

Cooling Trip Recovery

Silicon must not get warmed up unnecessarily: re-establish flow a.s.a.p., most importantly to SVX and L00!

- ___ 1) Find out what tripped from iFix interlock pages:
 - ___ a) From main menu: Silicon → SVX Interlock (for SVX/L00), ISL Interlock (for ISL)
 - ___ b) Logic at a glance:
 - ___ i) 2 kinds of enables: flow enables and power supply (PS) enables
 - ___ ii) Flow enable dropped: PS enable dropped automatically
 - ___ iii) 2 kinds of interlock inputs: global (e.g. chiller trip, PLC problem) and local (low flow, high temperature etc.)
 - ___ iv) Granularity of flow enables: Bulkheads for SVX, West/East for ISL, entire L00
 - ___ v) Granularity of PS enables: Bulkheads
 - ___ c) Questions to be answered for a trip:
 - ___ i) What has caused the trip (first fault in interlock chain)?
 - ___ ii) Did only the PS enable trip or also the flow enable?
- ___ 2) Press 'Acknowledge Trip' buttons to reset tripped coils in PLC
 - ___ a) Separate buttons for SVX, L00 (on 'SVX Interlocks') and ISL (on 'ISL Interlocks'), acknowledge L00 first
 - ___ b) All flow enable trips must be cleared before flow can be re-established
 - ___ c) All PS enable trips must be cleared (and the PS enable must be in "ENABLED") before Silicon can be powered
- ___ 3) Flow enable tripped: open e-valves from iFix SVX/ISL Flow Monitor pages (no login necessary)
 - ___ a) Default valve settings are posted on Flow Monitor page
 - ___ b) SVX: go straight to default valve settings
 - ___ c) ISL: all cooling lines of East/West are connected, sensitive to large pressure gradients
 - ___ i) Open e-valves in 5% steps starting from 30%
 - ___ ii) Try to equalize COT fact pressures

After one unsuccessful attempt to re-establish flow contact the Cooling Expert via pager (630.218.8626).

Date/Time _____

Name _____

Signed _____